

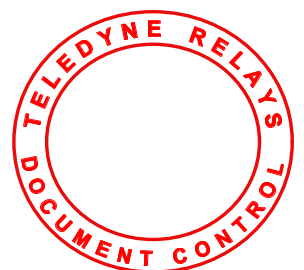
**DETAIL SPECIFICATION FOR  
HIREL S422 SURFACE MOUNT  
RELAYS**


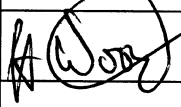

**RELAYS, HIGH RELIABILITY,  
ELECTROMECHANICAL, LATCHING, DPDT,  
LOW LEVEL TO 1.0 AMPERE,  
WITH OPTIONAL DIODE(S) FOR  
COIL TRANSIENT SUPPRESSION AND  
POLARITY REVERSAL PROTECTION**

**TR-HIREL-1/S422**

**ISSUE 1**

**20 Mar 2006**



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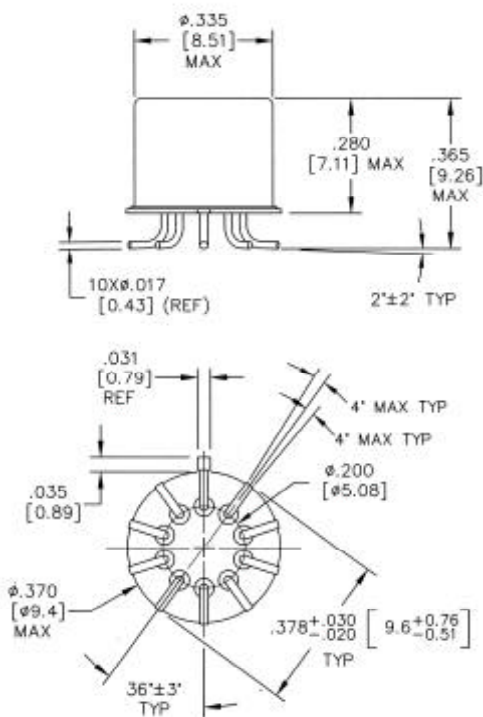
**DETAIL SPECIFICATION FOR S422 RELAYS**

**Relays, Surface Mount, High Reliability, Electromechanical, Latching, DPDT, Low Level to 1.0 Ampere, With Optional Diode(s) For Coil Transient Suppression**

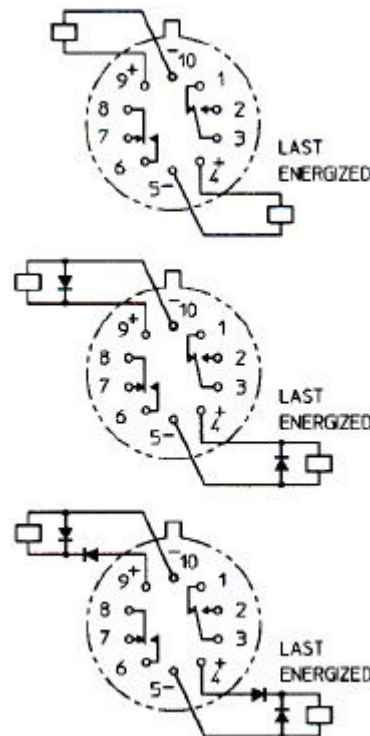
**1. SCOPE**

The performance and testing specifications for the above-referenced product are set forth in this detail specification. References to TR-HIREL-1 are to the most recent version thereof; in the event of conflict between this detail specification and TR-HIREL-1, the specifications set forth in this detail specification shall prevail.

**Figure 1  
Outline Dimensions  
Terminal View**



**Figure 2  
Terminal Locations and  
Circuit Diagram (bottom view)**



Basic relay

Relay with coil transient suppression diode

Relay with coil transient suppression and polarity reversal protection diodes

**Notes:**

1. The formed leads configuration is shown in Figure 1.
2. Dimensions are in inches. Metric equivalents in mm are given in parentheses.
3. Unless otherwise specified, the tolerance on dimensions is  $\pm 0.010$  in. ( $\pm 0.254$  mm).
4. Circuit diagrams shown in Figure 2 are terminal views (as seen from the bottom of the relays).
5. Terminal numbers are not marked on the relays.

**2. RELAY CHARACTERISTICS**

**2.1. General Data**

**2.1.1. Contact arrangement**

DPDT (2 Form C).

**2.1.2. Temperature range**

-65 °C to +125 °C.

**2.1.3. Dimensions and configuration**

See Figure 1 and 2.

**2.1.4. Weight**

2.84 g (0.10 oz) maximum.

**2.1.5. Seal**

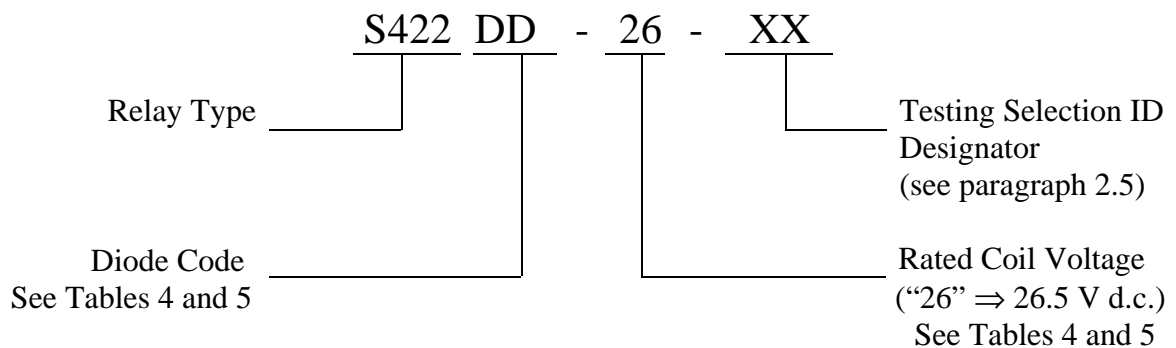
Hermetic Leak rate  $1 \times 10^{-8}$  atm-cm<sup>3</sup>/s of air maximum.

**2.1.6. Finish of the terminals**

Terminals are solder coated with Sn60 or Sn63, solder per ANSI J-STD-006. Refer to Figure 1 herein and TR-HIREL-1/Supplement 1.

**2.2. Part Number (ordering information)**

See TR-HIREL-1/Supplement 1 for checklist to be used for procurement.



**2.3. Contact Data**
**2.3.1. Contact load and life ratings**

See Table 1.

**2.3.2. Static contact resistance or voltage drop**

See Table 2. Contact resistance measurements shall be made at approximately  $\frac{1}{8}$  in. from the emergence of the lead from the seating plane of the relay.

**2.3.3. Contact bounce time**

When specified, see TR-HIREL-1/Supplement 1.  
2.0 ms maximum.

**2.3.4. Contact stabilization time**

When specified, see TR-HIREL-1/Supplement 1.  
2.5 ms maximum.

**Table 1 Contact Load and Life Ratings**

Load level	Contact load <u>1/</u>	Contact life
Low level/Mechanical	10 – 50 $\mu$ A at 10 – 50 mV d.c. or peak a.c.	100,000 cycles rated life
		1,000,000 cycles unmonitored (Mechanical life)
Intermediate current	100 mA at 28 V d.c.	50,000 cycles
High level, resistive	1.0 A at 28 V d.c.	100,000 cycles
High level, inductive	200 mA at 28 V d.c., with 0.32 H inductance	100,000 cycles
High level, lamp	100 mA at 28 V d.c.	100,000 cycles
Overload, resistive	2.0 A at 28 V d.c.	100 cycles
<b>Notes:</b> <u>1/</u> Relay case is grounded, unless otherwise specified.		

**Table 2 Static Contact Resistance or Voltage Drop**

<b>Measurement Condition</b>		<b>Maximum static contact resistance or voltage drop</b>
Initial without lead forming		0.125 $\Omega$ Max.
With formed leads		0.150 $\Omega$ Max.
Low level life	during life	33 $\Omega$ (1.65 mV d.c. monitoring level)
	after 100,000 or 1,000,000 cycle life	0.175 $\Omega$
Intermediate current	during test	1 $\Omega$ (100 mV d.c. monitoring level)
	after 50,000 cycle test	0.225 $\Omega$
High level life	during life	voltage drop no more than 5 % of open circuit voltage (1.4 V d.c. monitoring level)
	after 100,000 cycle life	0.225 $\Omega$
Overload	during life	not monitored
	after 100 cycle life	0.225 $\Omega$



## 2.4. Electrical Data

### 2.4.1. Insulation resistance

Points of measurement are as specified in Table 3. Coil leads of relays supplied with internal diode(s) should be connected together to avoid damage to the diode(s).

10,000 M $\Omega$  minimum at 500 V d.c., except as follows:

- 1,000 M $\Omega$  minimum at 500 V d.c. between coil and case at +125 °C
- 1,000 M $\Omega$  minimum at 500 V d.c. after 100 cycle overload, 100,000 cycle high level life, or 50,000 cycle intermediate current tests

### 2.4.2. Dielectric withstanding voltage

Points of application are as specified in Table 3. Coil leads of relays supplied with internal diode(s) should be connected together to avoid damage to the diode(s).

2.4.2.1. At atmospheric pressure:

500 V r.m.s.  $\pm$  5 % at 50 or 60 Hz (as applicable), except as follows:

- 375 V r.m.s. at 50 or 60 Hz (as applicable) after 100 cycle overload, 100,000 cycle high level life, or 50,000 cycle intermediate current tests.

**Table 3 Points of Application and Measurement for Insulation Resistance and Dielectric Withstanding Voltage Tests**

<b>Points of Application / Measurement</b>	<b>Reset Position</b>	<b>Set Position</b>
Between case, frame, or enclosure, and all contacts	✓	✓
Between case, frame, or enclosure and coils	✓	
Between all contacts and coils	✓	
Between open contacts	✓	✓
Between contact poles	✓	✓
Between coils	✓	

**2.4.3. Coil data and operating characteristics**

See Table 4 and 5.

**2.4.4. Timing (over the temperature range)****2.4.4.1. Operate time**

2.0 ms maximum with rated coil voltage.

**2.4.5. Diode characteristics****2.4.5.1. Coil transient suppression**

1.0 V d.c. maximum negative transient.

**2.4.5.2. Block integrity maximum leakage current**

1  $\mu$ A at 50 V d.c.

**2.4.5.3. Breakdown voltage**

100 V d.c. minimum at 10  $\mu$ A.

**Table 4 Coil Data and Operating Characteristics of Basic Relays and of Relays with Optional Diode for Coil Transient Suppression**

Coil voltage (V d.c.) <u>2/</u> <u>3/</u>		Room ambient temperature (+25 °C)		Over temperature range	Coil transient suppression diode code  <u>5/</u>
Rated	Max	Coil resistance (W) ± 10 %	Set/reset voltage (V d.c.) max <u>4/</u>	Set/reset voltage (V d.c.) max <u>4/</u>	
5.0	6.0	61	2.8	3.5	D
6.0	8.0	120	3.5	4.5	D
9.0	12.0	280	5.3	6.8	D
12.0	16.0	500	7.0	9.0	D
18.0	24.0	1130	10.5	13.5	D
26.5	32.0	2000	14.2	18.0	D

**Notes:**

1. Each relay possesses high level and low level capabilities. However, relays previously tested or used above 10 mA resistive at 6 V d.c. maximum or peak a.c. open circuit are not recommended for subsequent use in low level applications.
- 2/ When relays are installed in equipment, the coils should not be pulsed simultaneously. Coils should not be pulsed with less than the rated coil voltage and the pulse width should be a minimum of three times the specified operate time of the relay. If these conditions are not followed, it is possible for the relay to be in the magnetically neutral position.
- 3/ The use of any coil voltage other than the rated coil voltage will affect the electrical and dynamic characteristics of the relay.
- 4/ Relay contacts shall transfer to the set/reset position at a voltage no greater than the maximum set/reset voltage.
- 5/ No code letter for relays without diode(s).

**Table 5 Coil Data and Operating Characteristics of Relays with Internal Diodes for Coil Transient Suppression and Polarity Reversal Protection**

Coil voltage (V d.c.) <u>2/</u> <u>3/</u>		Room ambient temperature (+25 °C)			Set/reset voltage (V d.c.) max <u>5/</u>	Over temp- erature range	Coil transient suppression and polarity reversal protection diode code
		Coil resistance (ref. only) (W) <u>4/</u>	Coil circuit current (mA) <u>4/</u>				
Rated	Max		Max	Min			
5.0	6.0	48	104.2	75.8	3.5	4.5	DD
6.0	8.0	97	63.0	46.9	4.1	5.5	DD
9.0	12.0	280	33.7	26.0	6.3	7.8	DD
12.0	16.0	500	25.5	20.0	8.0	10.0	DD
18.0	24.0	1130	17.2	13.7	11.6	14.5	DD
26.5	32.0	2000	14.4	11.6	15.4	19.0	DD

**Notes:**

1. Each relay possesses high level and low level capabilities. However, relays previously tested or used above 10 mA resistive at 6 V d.c. maximum or peak a.c. open circuit are not recommended for subsequent use in low level applications.
- 2/ When latching relays are installed in equipment, the coils should not be pulsed simultaneously. Coils should not be pulsed with less than the rated coil voltage and the pulse width should be a minimum of three times the specified operate time of the relay. If these conditions are not followed, it is possible for the relay to be in the magnetically neutral position.
- 3/ The use of any coil voltage other than the rated coil voltage will affect the electrical and dynamic characteristics of the relay.
- 4/ Coil resistance is not directly measurable at relay terminals; coil current shall be measured in lieu of coil resistance.
- 5/ Relay contacts shall transfer to the set/reset position at a voltage no greater than the maximum set/reset voltage.

## 2.5. Testing Selection ID Designator

The Testing Section ID Designator is an alphanumeric field that represents the combination of manufacturing and/or screening variants indicated below that are specified by the Orderer. Each combination of manufacturing and/or screening variants has a unique ID designator which is part of the Part Number (see 2.2). It allows the Orderer to select from specified manufacturing and/or screening variants based on program requirements.

### 2.5.1. Manufacturing Variants and Processes

Prior to encapsulation, when specified (see TR-HIREL-1/Supplement 1), the relays shall be subjected to the tests/inspections specified in Table 6.

### 2.5.2. Screening Variants

When specified (see TR-HIREL-1/Supplement 1), the relays shall be tested for the following attributes or shall be subjected to the tests/inspections of Table 7 as part of Quality Conformance Inspection.

Refer to see TR-HIREL-1/Supplement 1 for a further breakdown of Lot Acceptance Test Level 2 and 1.

The process of surface mount lead forming may be performed during or after Screening and Electrical Measurements and Lot Acceptance Tests at the manufacturers option. Relays subjected to this process will meet the electrical characteristics and the hermetic seal, requirements of this specification.

**Table 6 Manufacturing Variants**

<b>Test</b>	<b>Requirement paragraph in TR-HIREL-1</b>	<b>Test method paragraph in TR-HIREL-1</b>
Small Particle Inspection	3.1	4.11.2

**Table 7 Screening Variants**

<b>Test</b>	<b>Requirement paragraph in TR-HIREL-1</b>	<b>Test method paragraph in TR-HIREL-1</b>
Solderability	3.6.2	4.11.4
Vibration (random)	3.6.6	4.11.11.2
Vibration miss test	3.6.7	4.11.12
Particle impact noise detection	3.6.21	4.11.23
Contact bounce time	3.4.2.1	4.11.8.5.2
Contact stabilization time	3.4.2.2	4.11.8.5.3
Lot Acceptance Tests (Level 3)	3.1	4.10.3
Lot Acceptance Tests (Level 2)	3.1	4.10.3
Lot Acceptance Tests (Level 1)	3.1	4.10.3

**2.6. Environmental Data**

The relays shall withstand the environmental tests as specified in Table 8.

**Table 8 Environmental Tests**

<b>Test</b>	<b>Requirement Paragraph in TR-HIREL-1</b>	<b>Test Method Paragraph in TR-HIREL-1</b>	<b>Details and Exceptions</b>
Solderability	3.6.2	4.11.4	
Thermal shock	3.6.4	4.11.9	
Shock (specified pulse)	3.6.5	4.11.10	Test condition C (100 G peak half-sine, 6 ms duration)
Vibration (sinusoidal)	3.6.6	4.11.11.1	
Vibration (random)	3.6.6	4.11.11.2	
Terminal strength	3.6.8	4.11.20	
Coil life	3.6.9	4.11.21	
Resistance to soldering heat	3.6.10	4.11.17	
Salt atmosphere (corrosion)	3.6.11	4.11.13	Performance requirement
Resistance to solvents	3.6.16	4.11.16	

**3. MARKING**

Marking on the relay includes Teledyne Relays' Part Number (see 2.2), Lot Number, Circuit Diagram, Teledyne Relays' Name, Serial Number and Contact Current Rating. The circuit diagram as marked on the relay is the terminal view.

**4. QUALITY ASSURANCE**

Quality assurance provisions are as specified in TR-HIREL-1. The following details shall apply:

**4.1. Final Production Tests**

See Table 9.

**4.2. Screening and Electrical Measurements**

See Table 10.

**4.3. Lot Acceptance Tests**

When specified, see TR-HIREL-1/Supplement 1

See Tables 11, 12 and 13.



**Table 9 Final Production Tests 1/**

Inspection	Requirement in TR-HIREL-1	Test method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
1. Verification of precap inspection, customer source inspection - precap inspection, Marking/serialization	3.6.18	4.11.1.1	2.2	✓	✓	✓
	3.6.17		3.0			
2. Verification of small particle inspection (when specified)	3.1	4.11.2	2.5.1	✓	✓	✓
3. Room temperature electrical measurements <u>2/</u>						
a) Coil resistance	3.5.4	4.11.8.2	2.4.3	✓	✓	
b) Coil current	3.5.5	4.11.8.3	2.4.3			✓
c) Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
d) Dielectric withstanding voltage (atmospheric pressure)	3.5.2	4.11.7	2.4.2	✓	✓	✓
e) Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
f) Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
g) Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
h) Operate contact bounce time (when specified)	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
i) Operate contact stabilization time (when specified)	3.4.2.2	4.11.8.5.3	2.3.4, 2.5.2	✓	✓	✓
j) Coil transient suppression	3.5.7.1	4.11.8.6.1	2.4.5.1		✓	✓
k) Block integrity	3.5.7.2	4.11.8.6.2	2.4.5.2			✓
l) Neutral Screen	3.5.8	4.11.8.7		✓	✓	✓
4. Solderability (when specified) <u>3/</u>	3.6.2	4.11.4	2.5.2, 2.6	✓	✓	✓
5. Seal	3.6.3	4.11.5	2.1.5	✓	✓	✓
6. Visual inspection, external <u>4/</u>	3.6.18	4.11.1.2	2.1.3, 2.1.4, 2.1.6, 2.2	✓	✓	✓

**Notes:**

1/ 100% inspection applies, unless otherwise noted. For 100 % inspection, discard all failed relays.

2/ Test sequence is optional.

3/ Perform on 2 relays from each lot. Failed relays resulting from Room Temperature Electrical Measurements may be used for test.

4/ Physical dimensions and weight shall be measured on two sample units per lot.

**Table 10 Screening and Electrical Measurements 1/ 3/**

Inspection	Requirement in TR-HIREL-1	Test method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
1. Vibration, sinusoidal	3.6.6	4.11.11.1	2.5.2	✓	✓	✓
2. Vibration, random (when specified)	3.6.6	4.11.11.2	2.5.2, 2.6	✓	✓	✓
3. Vibration miss test (when specified)	3.6.7	4.11.12	2.5.2	✓	✓	✓
4. Particle impact noise detection (PIND) test (when specified)	3.6.21	4.11.23	2.5.2	✓	✓	✓
5. Internal moisture	3.6.1	4.11.3.1		✓	✓	✓
6. Thermal cycle/Miss test	3.6.19	4.11.3.2				
First four hot/cold cycles:						
a) Coil continuity		4.11.3.2.1		✓	✓	✓
Fifth hot/cold cycle:						
b) High temperature soak		4.11.3.2.1		✓	✓	✓
c) High temperature electrical measurements <u>2/</u>		4.11.3.2.1				
i. Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
ii. Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
iii. Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
iv. Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
v. Operate contact bounce time	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
d) High temperature Miss test	3.6.1	4.11.3.2.1, 4.11.3.2.2		✓	✓	✓
e) Low temperature soak		4.11.3.2.1		✓	✓	✓
f) Low temperature electrical measurements <u>2/</u>		4.11.3.2.1				
i. Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
ii. Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
iii. Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
iv. Operate contact bounce time	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓

See notes at end of Table.

**Table 10 Screening and Electrical Measurements (cont'd) 1/ 3/**

Inspection	Requirement in TR-HIREL-1	Test method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
7. Thermal cycle/Miss test (cont'd)						
g) Low temperature Miss test	3.6.1	4.11.3.2.1, 4.11.3.2.2		✓	✓	✓
h) Stabilize at room ambient temperature		4.11.3.2.1		✓	✓	✓
i) Room temperature Miss test	3.6.1	4.11.3.2.1, 4.11.3.2.2		✓	✓	✓
8. Room temperature electrical measurements <u>2/</u>						
a) Coil resistance	3.5.4	4.11.8.2	2.4.3	✓	✓	
b) Coil current	3.5.5	4.11.8.3	2.4.3			✓
c) Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
d) Dielectric withstanding voltage (atmospheric pressure)	3.5.2	4.11.7	2.4.2	✓	✓	✓
e) Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
f) Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
g) Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
h) Operate contact bounce time (when specified)	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
i) Operate contact stabilization time (when specified)	3.4.2.2	4.11.8.5.3	2.3.4, 2.5.2	✓	✓	✓
j) Coil transient suppression	3.5.7.1	4.11.8.6.1	2.4.5.1		✓	✓
k) Block integrity	3.5.7.2	4.11.8.6.2	2.4.5.2			✓
l) Neutral Screen	3.5.8	4.11.8.7		✓	✓	✓
9. Seal	3.6.3	4.11.5	2.1.5	✓	✓	✓
10. Radiographic inspection	3.6.20	4.11.22		✓	✓	✓
11. Visual inspection, external	3.6.18	4.11.1.2	2.1.3, 2.1.4, 2.1.6, 2.2	✓	✓	✓
12. Check for lot failure		4.8		✓	✓	✓

**Notes:**

1/ Inspection sample 100 % unless otherwise noted; discard all failed relays.

2/ Test sequence is optional.

3/ Surface Mount Leads Forming may be performed during, or after Screening and Electrical Measurements. An electrical inspection, and seal test as applicable, shall follow any of these processes.

**Table 11 Lot Acceptance Test 3 1/**

Inspection	Requirement in TR-HIREL-1	Test Method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
1. Room temperature electrical measurements <u>2/</u>						
a) Coil resistance	3.5.4	4.11.8.2	2.4.3	✓	✓	
b) Coil current	3.5.5	4.11.8.3	2.4.3			✓
c) Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
d) Dielectric withstanding voltage (atmospheric pressure)	3.5.2	4.11.7	2.4.2	✓	✓	✓
e) Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
f) Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
g) Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
h) Operate contact bounce time (when specified)	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
i) Operate contact stabilization time (when specified)	3.4.2.2	4.11.8.5.3	2.3.4, 2.5.2	✓	✓	✓
j) Coil transient suppression	3.5.7.1	4.11.8.6.1	2.4.5.1		✓	✓
k) Block integrity	3.5.7.2	4.11.8.6.2	2.4.5.2			✓
l) Neutral Screen	3.5.8	4.11.8.7				
2. Seal	3.6.3	4.11.5	2.1.5	✓	✓	✓
3. Visual inspection, external	3.6.18	4.11.1.2	2.1.3, 2.1.4, 2.1.6, 2.2	✓	✓	✓
4. Check for lot failure		4.8		✓	✓	✓
<b>Notes:</b>						
<u>1/</u> See TR-HIREL-1, Paragraph 4.10.3 and 2.5.2 herein.						
<u>2/</u> Test sequence is optional.						

**Table 12 Lot Acceptance Test 2 1**

Inspection	Requirement in TR-HIREL-1	Test method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
<b>Group I</b>						
1. Life <u>1</u>	3.6.13	4.11.19	2.3.1, 2.5.2	✓	✓	✓
2. Room temperature electrical measurements <u>2</u>						
a) Coil resistance	3.5.4	4.11.8.2	2.4.3	✓	✓	
b) Coil current	3.5.5	4.11.8.3	2.4.3			✓
c) Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
d) Dielectric withstanding voltage (atmospheric pressure)	3.5.2	4.11.7	2.4.2	✓	✓	✓
e) Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
f) Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
g) Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
h) Operate contact bounce time (when specified)	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
i) Operate contact stabilization time (when specified)	3.4.2.2	4.11.8.5.3	2.3.4, 2.5.2	✓	✓	✓
j) Coil transient suppression	3.5.7.1	4.11.8.6.1	2.4.5.1		✓	✓
k) Block integrity	3.5.7.2	4.11.8.6.2	2.4.5.2			✓
l) Neutral Screen	3.5.8	4.11.8.7		✓	✓	✓
3. Visual inspection, external <u>3</u>	3.6.18	4.11.1.2	2.1.3, 2.1.4, 2.1.6, 2.2	✓	✓	✓
4. Check for lot failure		4.8		✓	✓	✓

See notes at end of Table.

**Table 12 Lot Acceptance Test 2 (cont'd) 1/**

Inspection	Requirement in TR-HIREL-1	Test method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
<b>Group II</b>						
1. Intermediate current <u>1/</u>	3.6.14	4.11.14	2.3.1, 2.5.2	✓	✓	✓
2. Room temperature electrical measurements <u>2/</u>						
a) Coil resistance	3.5.4	4.11.8.2	2.4.3	✓	✓	
b) Coil current	3.5.5	4.11.8.3	2.4.3			✓
c) Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
d) Dielectric withstanding voltage (atmospheric pressure)	3.5.2	4.11.7	2.4.2	✓	✓	✓
e) Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
f) Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
g) Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
h) Operate contact bounce time	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
i) Coil transient suppression	3.5.7.1	4.11.8.6.1	2.4.5.1		✓	✓
j) Block integrity	3.5.7.2	4.11.8.6.2	2.4.5.2			✓
k) Neutral Screen	3.5.8	4.11.8.7		✓	✓	✓
3. Visual Inspection, external <u>3/</u>	3.6.18	4.11.1.2	2.1.3, 2.1.4, 2.1.6, 2.2	✓	✓	✓
4. Check for lot failure		4.8		✓	✓	✓

See notes at end of Table.

**Table 12 Lot Acceptance Test 2 (cont'd) 1/**

Inspection	Requirement in TR-HIREL-1	Test method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
<b>Group III</b>						
1. Overload (highest dc resistive load) <u>1/</u>	3.6.12	4.11.18	2.3.1, 2.5.2	✓	✓	✓
2. Room temperature electrical measurements <u>2/</u>						
a) Coil resistance	3.5.4	4.11.8.2	2.4.3	✓	✓	
b) Coil current	3.5.5	4.11.8.3	2.4.3			✓
c) Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
d) Dielectric withstanding voltage (atmospheric pressure)	3.5.2	4.11.7	2.4.2	✓	✓	✓
e) Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
f) Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
g) Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
h) Operate contact bounce time	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
i) Coil transient suppression	3.5.7.1	4.11.8.6.1	2.4.5.1		✓	✓
j) Block integrity	3.5.7.2	4.11.8.6.2	2.4.5.2			✓
k) Neutral Screen	3.5.8	4.11.8.7		✓	✓	✓
3. Visual Inspection, external <u>3/</u>	3.6.18	4.11.1.2	2.1.3, 2.1.4, 2.1.6, 2.2	✓	✓	✓
4. Check for lot failure		4.8		✓	✓	✓

See notes at end of Table.

**Table 12 Lot Acceptance Test 2 (cont'd) 1/**

Inspection	Requirement in TR-HIREL-1	Test method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
<b>Group IV</b>						
1. Mechanical life <u>1/</u>	3.6.15	4.11.15	2.3.1, 2.5.2	✓	✓	✓
2. Room temperature electrical measurements <u>2/</u>						
a) Coil resistance	3.5.4	4.11.8.2	2.4.3	✓	✓	
b) Coil current	3.5.5	4.11.8.3	2.4.3			✓
c) Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
d) Dielectric withstanding voltage (atmospheric pressure)	3.5.2	4.11.7	2.4.2	✓	✓	✓
e) Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
f) Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
g) Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
h) Operate contact bounce time (when specified)	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
i) Operate contact stabilization time (when specified)	3.4.2.2	4.11.8.5.3	2.3.4, 2.5.2	✓	✓	✓
j) Coil transient suppression	3.5.7.1	4.11.8.6.1	2.4.5.1		✓	✓
k) Block integrity	3.5.7.2	4.11.8.6.2	2.4.5.2			✓
l) Neutral Screen	3.5.8	4.11.8.7		✓	✓	✓
3. Visual Inspection, external <u>3/</u>	3.6.18	4.11.1.2	2.1.3, 2.1.4, 2.1.6, 2.2	✓	✓	✓
4. Check for lot failure		4.8		✓	✓	✓
<b>Notes:</b>						
<u>1/</u> See TR-HIREL-1, Paragraph 4.10.3 and 2.5.2 herein.						
<u>2/</u> Test sequence is optional.						
<u>3/</u> The header glass criteria is not applicable.						
4. Post-life operating characteristics, operate time, and contact dynamic characteristics, as applicable, are allowed to have a 10% variance from the initial allowable values.						



**Table 13 Lot Acceptance Test 1 1/**

Inspection	Requirement in TR-HIREL-1	Test method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
<b>Group I</b>						
1. Thermal shock	3.6.4	4.11.9		✓	✓	✓
2. Shock (specified pulse)	3.6.5	4.11.10	2.6	✓	✓	✓
3. Vibration (sinusoidal and random)	3.6.6	4.11.11	2.6	✓	✓	✓
4. Terminal strength	3.6.8	4.11.20	2.6	✓	✓	✓
5. Room temperature electrical measurements <u>2</u> /						
a) Coil resistance	3.5.4	4.11.8.2	2.4.3	✓	✓	
b) Coil current	3.5.5	4.11.8.3	2.4.3			✓
c) Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
d) Dielectric withstanding voltage (atmospheric pressure)	3.5.2	4.11.7	2.4.2	✓	✓	✓
e) Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
f) Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
g) Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
h) Operate contact bounce time (when specified)	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
i) Operate contact stabilization time (when specified)	3.4.2.2	4.11.8.5.3	2.3.4, 2.5.2	✓	✓	✓
j) Coil transient suppression	3.5.7.1	4.11.8.6.1	2.4.5.1		✓	✓
k) Block integrity	3.5.7.2	4.11.8.6.2	2.4.5.2			✓
l) Neutral Screen	3.5.8	4.11.8.7		✓	✓	✓
6. Seal	3.6.3	4.11.5	2.1.5	✓	✓	✓
7. Visual inspection, external <u>3</u> /	3.6.18	4.11.1.2	2.1.3, 2.1.4, 2.1.6, 2.2	✓	✓	✓
8. Check for lot failure		4.8		✓	✓	✓

See notes at end of Table.

**Table 13 Lot Acceptance Test 1 (cont'd) 1/**

Inspection	Requirement in TR-HIREL-1	Test method in TR-HIREL-1	Details and exceptions in this spec	Diode Codes		
				None	D	DD
<b>Group II</b>						
1. Coil life	3.6.9	4.11.21	2.6	✓	✓	✓
2. Resistance to solvents	3.6.16	4.11.16	2.6	✓	✓	✓
3. Resistance to soldering heat	3.6.10	4.11.17	2.6	✓	✓	✓
4. Room temperature electrical measurements <u>2/</u>						
a) Coil resistance	3.5.4	4.11.8.2	2.4.3	✓	✓	
b) Coil current	3.5.5	4.11.8.3	2.4.3			✓
c) Insulation resistance	3.5.1	4.11.6	2.4.1	✓	✓	✓
d) Dielectric withstanding voltage (atmospheric pressure)	3.5.2	4.11.7	2.4.2	✓	✓	✓
e) Static contact resistance	3.4.1.1	4.11.8.5.1	2.3.2	✓	✓	✓
f) Set/reset voltage	3.5.3.2	4.11.8.1.2	2.4.3	✓	✓	✓
g) Operate time	3.5.6	4.11.8.4	2.4.4	✓	✓	✓
h) Operate contact bounce time (when specified)	3.4.2.1	4.11.8.5.2	2.3.3, 2.5.2	✓	✓	✓
i) Operate contact stabilization time (when specified)	3.4.2.2	4.11.8.5.3	2.3.4, 2.5.2	✓	✓	✓
j) Coil transient suppression	3.5.7.1	4.11.8.6.1	2.4.5.1		✓	✓
k) Block integrity	3.5.7.2	4.11.8.6.2	2.4.5.2			✓
l) Neutral Screen	3.5.8	4.11.8.7		✓	✓	✓
5. Visual inspection, external <u>3/</u>	3.6.18	4.11.1.2	2.1.3, 2.1.4, 2.1.6, 2.2	✓	✓	✓
6. Check for lot failure		4.8		✓	✓	✓

**Notes:**

1/ See TR-HIREL-1 Paragraph 4.10.3 and 2.5.2 herein.

2/ Test sequence is optional.

3/ The header glass criteria is not applicable.

4 Post-life operating characteristics, operate time, and contact dynamic characteristics, as applicable, are allowed to have a 10% variance from the initial allowable values.

**5. GENERAL NOTES**

- a) Reverse polarity on the coil terminals of relays supplied with internal diode for coil transient suppression (without polarity reversal protection) will destroy the diode.
- b) When used in applications at high level loads, surge current protection is recommended.

**Appendix A Document Change History**

<b>Issue or Amendment No.</b>	<b>Date of Issue</b>	<b>Reference and Change Description</b>	<b>Change Notice No.</b>
Issue 1	20 Mar 2006	Official Release	28426